

**ICMEET - 2022**

**Call for Papers**

**Special Session on**

**“Recent Trends in AI Signal Processing Integrated Circuits (ASPIC)**

**Session Chair(s):**

**Dr. N. Udaya Kumar**, Professor & Head, Department of E.C.E, S.R.K.R Engineering College, Bhimavaram-534202.

*E-mail: [udayvas2005@gmail.com](mailto:udayvas2005@gmail.com)*

**Dr. K. Bala Sindhuri**, Assistant Professor, Department of E.C.E, S.R.K.R Engineering College, Bhimavaram-534202.

*E-mail: [k.b.sindhuri@gmail.com](mailto:k.b.sindhuri@gmail.com)*

## **Theme of Session:**

The main aim of this special session is to bring together research professional fraternity for the discussions and exchange of information on advances in VLSI, Testing and Testing circuits, Data trends in VLSI, VLSI with Artificial Intelligence, VLSI with Machine Learning and Signal Processing applications.

This session is an outstanding platform to discuss the key findings, exchanging novel ideas, listening to the world class researchers, and sharing experiences with peer groups.

## **Topics of Interest:**

Based on the above-mentioned theme, the topics of interest are (but not limited to):

- Low Power and Low- Voltage VLSI Circuits
- High Speed VLSI Circuits
- Design of Semiconductor Memories
- VLSI Signal Processing
- Physical Design Automation

- VLSI Testing & Testable circuits
- ASIC/FPGA Design
- Analog, Digital & Mixed Signal Design
- Testing & Testable Circuit
- VLSI for signal processing
- Hardware Security and VLSI Design Optimization
- Signal Processing Systems and Architectures
- VLSI for AI & ML
- Data Trends in VLSI
- Hardware -Software Co-Design
- Embedded Hardware Security and Cryptography
- MEMS, NEMS design and technology

### **Technical Committee:**

1. Dr. A M Prasad, Professor, ECE Dept., UCEK, JNTU, Kakinada.
2. Dr. N. Balaji, Professor, Director, IQAC, University College of Engineering, JNTUK, Kakinada.
3. Dr T S N Murthy, Assistant Professor, Dept of ECE, JNTUGV, Vizianagaram.
4. Dr. K. Aparna, Assistant Professor, Dept of ECE, JNTUACE, Kalikiri.
5. Dr. E. Gnanamanoharan, Assistant Professor, Dept of ECE, Annamalai university, Chidambaram.TN.
6. Dr. Sujan Kasani, Product Development R&D Engineer, Intel Corporation, USA.
7. Dr. Soma S Dhavala, Principal ML Scientist, Wadhvani Institute for AI, Bangalore
8. Mr. S. Narayana Rao, Founder / CTO, Salcit Technologies Pvt Ltd, Hyderabad
9. Mr. V. Siva Raghu Ram, MS VLSI, Sr. Director, Western Digital, Bangalore.
10. Mr.P.Nageswara Rao, M. Tech (Microelectronics), Manager (Semiconductors), Bharat Electronics Limited, Bangalore.

### **Paper Submission Process: (By Email to the following ID only)**

Please submit your paper (in word & pdf format) at Email: [kbs.aspic2022@gmail.com](mailto:kbs.aspic2022@gmail.com) , with “**Special Session- ASPIC**” mentioned in the subject line.

*For further queries related to this special session, please contact the session chair.*